



深圳市宇锡科技有限公司

SHENZHEN YOUSEE TECHNOLOG CO., LTD

DISPLAY SPECIFICATION

Product NO: (产品型号) YX43025A

Customer : (客 户) _____

APPROVED BY CUSTOMER 客户签署栏	
Approved by 审核	Remark 备注

APPROVED BY YOUSEE 宇锡签署栏			
Prepared by 制作	Checked by 检查		Approved by 审核
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**1. GENERAL INFORMATION****基本描述**

Item 项目	Contents 内容	Unit 单位
LCD Type LCD 类型	IPS TFT-LCD /TRANSMISSIVE	-
Number of Dots 点阵	480(RGB) * 272	Dot
Outline dimensions 外形尺寸	105.5(W)* 67.2(H)*3.0 (T)	mm ³
Active area 有效区域	95.04 (W)* 53.856 (H)	mm ²
Pixel pitch 像素	0.198*0.198	mm ²
Module size 模组尺寸	4.3	inch
Viewing direction 视角方向	ALL	O'clock
Backlight type 背光类型	White LED	-
LCD Driver IC LCD 驱动 IC	/	-
Interface type 接口类型	MCU8/9/16Bit; 3/4-line/Dual SPI	-



<p>6</p> <p>1. 批量产品成品亮度公差为±10%，测试条件 IF=20mA/LED, V=3.0V/3.3V/LED. 2. 批量产品成品色坐标公差为±0.03. 3. 批量产品均匀性80%以上.</p>	<p>C</p> <p>测试距离:500mm Measure distance:500mm 测量角度:1°, 9个点 Angle:1°, 9 Points 亮度值是用M-7色度仪测试 The Measurement instrument is :M-7 均匀性=最小/最大亮度值*100% Uniformity=min/max*100%</p> <p>设计注意事项: 1. 图中FRAME VA为推荐TP视窗位置、大小, 建议TP视窗位置、大小不要超过FRAME VA。 2. 整机外壳带泡棉产品, 建议客户机壳泡棉进入LCD单边0.5MM MIN, 防止漏光。 3. 整机机壳不带泡棉产品, 建议客户整机TP的丝印黑油开窗或者LENS丝印黑油开窗比LCD A.A区单边大0.6MM MAX。 4. 客户需保证触摸屏下表面到玻璃上表面最少0.3MM MIN(非贴合)。</p>	<p>B</p> <p>技术要求: 1) 液晶显示模式:4.3" TFT transmissive 2) 视角: IPS(全视角) 3) 点阵: 480 X 272 4) 驱动芯片型号: / 5) 工作温度:-20°-----70° 6) 未注尺寸公差:±0.2mm 7) 材料及工艺符合 ROHS</p>	<p>A</p>																																																																																
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**3. INTERFACE DESCRIPTION 接口定义**

NO.	PIN NAME	Description
1	LED-K	LED Cathode
2	LED-A	LED Anode
3	GND	Power Ground
4	SPI4W	Std SPI 3/4 wire selection. SPI4W="H", 4 wire SPI; \ SPI4W="L", 3wire SPI.
5~16	DB0~DB11	MCU parallel interface data bus
17	TE	Tearing effect output pin is used to synchronize MCU frame writing.
18~21	DB12~DB15	MCU parallel interface data bus
22	GND	Power Ground
23	RESET	This signal will reset the device and it must be applied to properly initialize the chip, signal is active low
24	WRX/SPI CLK	Write enable in MCU parallel interface; In Serial Interface, this is used as SCL.
25	SPI-SDA	Serial input/output signal in serial interface mode.
26	CS	Chip Selection Pin
27	GND	Power Ground
28	RDX	Read enable in 8080 MCU parallel interface. Fix to IOVCC level when not in use
29	DCX/SPI RS	Data/command selection pin in parallel/ 4-line serial interface.
30	GND	Power Ground
31	IM1~IM2	Select interface mode :MCU8/16Bit; 3/4-line/Dual SPI; QSPI .
33	VDDI	Power supply for digital interface I/O pins
34	VDD	LCM Power Supply .
35	GND	Power Ground
36	CTP_VDD	Touch Power supply, No connection.
37	CTP_RESET	Touch Reset Signal, No connection.
38	CTP_INT	Touch Interrupt signal, No connection.
39	CTP_SCL	Touch IIC Clock signal, No connection.
40	CTP_SDA	Touch IIC Data signal, No connection.



Note1: Selection of these interface are set by IM<1:0> , SPI4W pins as shown below Table:

SPI4W	IM1	IM0	Interface	Read Back Data Bus Selection
0	0	0	8080 series 8bit	DG3-2, DB7-2
			8080 series 9bit	DG4-2, DB7-2
			8080 series 16bit	DR5-2, DG7-2, DB7-2
0	0	1	3-wire Std SPI	SDA: In/Out
0	1	0	Dual SPI	SDA:In/Out DCX:In
0	1	1	Quad SPI	SDA:In/Out SDA/ SDO1 DCX:In DCX/SDO2 DB[2]:In SDO3 DB[3]:In SDO4
1	0	1	4 -wire Std SPI	SDA: In/Out ,DCX

**4. ABSOLUTE MAXIMUM RATINGS****极限参数(IC)**

Parameter 参数	Symbol 符号	Min 最小值	Max 最大值	Unit 单位
Supply voltage for logic 逻辑电压 I/O	IOVCC	-0.3	+3.6	V
Input voltage for analog circuit 输入模拟电压	VCC	-0.3	+3.6	V
Operating temperature 操作温度	TOP	-20	+70	°C
Storage temperature 存储温度	TST	-30	+80	°C
Humidity 湿度	RH	-	90%(60°C)	RH

5. ELECTRICAL CHARACTERISTICS**模块电气特性**

Parameter 参数	Symbol 符号	Min 最小指	Typ 典型值	Max 最大值	Unit 单位
Analog operating voltage 模拟电压	VCC	2.8	3.3	3.6	V
Logic operating voltage 逻辑电压	IOVCC	/	/	/	V
Input voltage “H”level 输入高电平	V _{IH}	0.7 *IOVCC	-	IOVCC	V
Input voltage “L”level 输入低电平	V _{IL}	GND	-	0.3* IOVCC	V
Output voltage “H”level 输出高电平	V _{OH}	0.8*IOVCC	-	IOVCC	V
Output voltage “L”level 输出低电平	V _{OL}	GND	-	0.2 *IOVCC	V

**6. BACKLIGHT CHARACTERISTICS**

背光电气特性

Item 项目	Symbol 符号	Min 最小值	Typ 中间值	Max 最大值	Unit 单位	Condition 条件
Forward voltage 正向电压	Vf	15	16	17	V (伏)	If=40mA, Ta=25°C
Number of LED LED 灯数	-	10			Piece (颗)	-
Backlight lifetime		30,000			Hrs	
Connection mode 连接类型	S/P	Series and Parallel (5S2P)			-	-

7. OPTICAL CHAYACTERISTICS

光学特性

Item 项目	Symbol 符号	Condition 条件	Min 最小值	Typ 典型值	Max 最大值	Unit 单位	Remark 注释	Note 备注
Response time 响应时间	Tr+Tf	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25°C	-	25	-	ms	FIG1	1
Contrast ratio 对比度	Cr		-	800	-	-	FIG2	2
Color gamut 饱和度	S(%)		-	60	-	%	-	-
Luminance uniformity 均匀度	δ WHITE		-	80	-	%	FIG2	3
Viewing angle range 视角范围	θ_{x+}	CR \geq 10 Ta=25°C	-	80	-	deg	FIG3	4
	θ_{x-}		-	80	-	deg	FIG3	
	θ_{y+}		-	80	-	deg	FIG3	
	θ_{y-}		-	80	-	deg	FIG3	
LCM 亮度	Lv	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25°C	450	500	-	Cd/m ²	FIG2	5
CIE (X,Y) Chromaticity 色度坐标	White(X)		-	TBD	-	-	FIG2	6
	White(Y)	-	TBD	-	-			

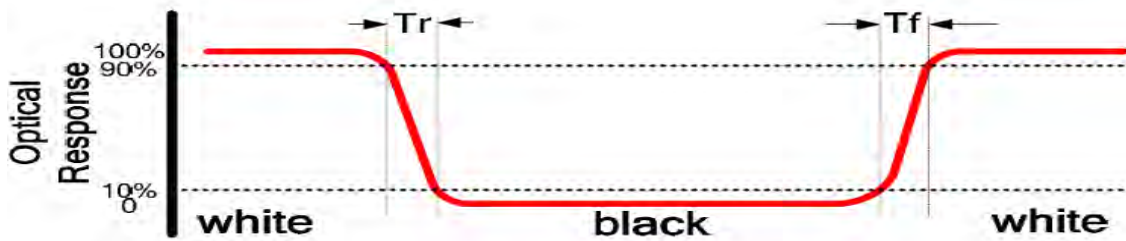


Note1.Response time is the time required for the display to transition from White to black(Rise Time,Tr)and from black to white(Decay Time,Tf).For additional information see FIG1...

备注 1. 响应时间是 Tr(上升时间) 与 Tf(下降时间) 的和, Tr 指显示黑色画面转为显示白色画面需要时间, Tf 指显示白色画面转为显示黑色画面需要时间, 详见 FIG1.。

FIG1. The definition of Response time

响应时间定义



Note2.contrast Ratio(CR) is defined mathematically by the following formula ,For more information see FIG2.

Contrast Ratio(CR)=Average Surface Luminance with all white pixels/ Average Surface Luminance with all black pixels

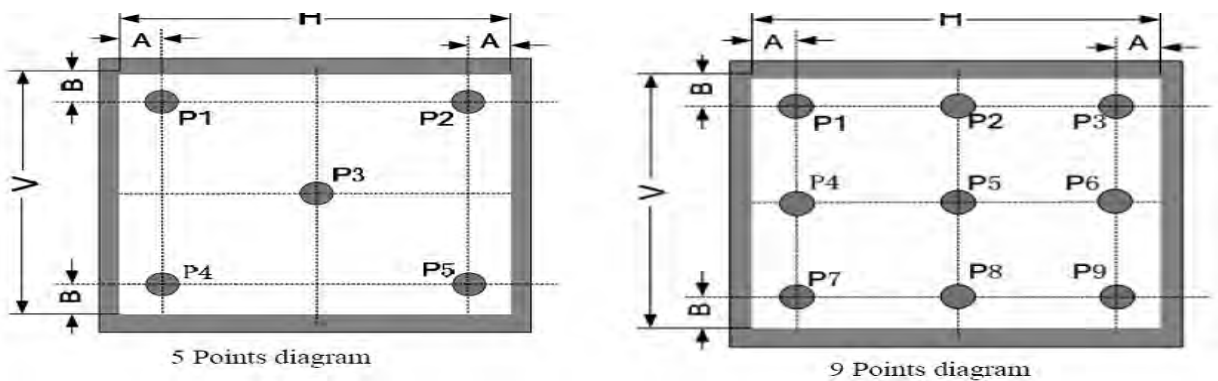
备注 2. 对比度是由以下公式计算所得。详见 FIG2.

对比度=显示白色画面时平均表面亮度 (P1, P2, ……)/显示黑色画面时平均表面亮度 (P1, P2, ……)

FIG2. Measuring method for Contrast ratio,surface luminance,Luminance uniformity,CIE(X,Y)

chromaticity.

对比度, 表面亮度, 均匀度, CIE 坐标测试方法



Note3.The uniformity in surface luminance(WHITE) is determined by measuring luminance at eath test position,and then dividing the maximum luminance of all white pixels by minimum luminance of all white pixels,For more information seeFIG2.

WHITE=Minimum Surface Luminance with all white pixels(P1,P2,……)/Maximum Surface Luminance with all white pixels(P1,P2,……)

备注 3. 均匀度是在显示白色画面时, 测试 P1 到 P9 的亮度, 然后再用 9 个点亮度的最小值除以最大值。详见 FIG2.。

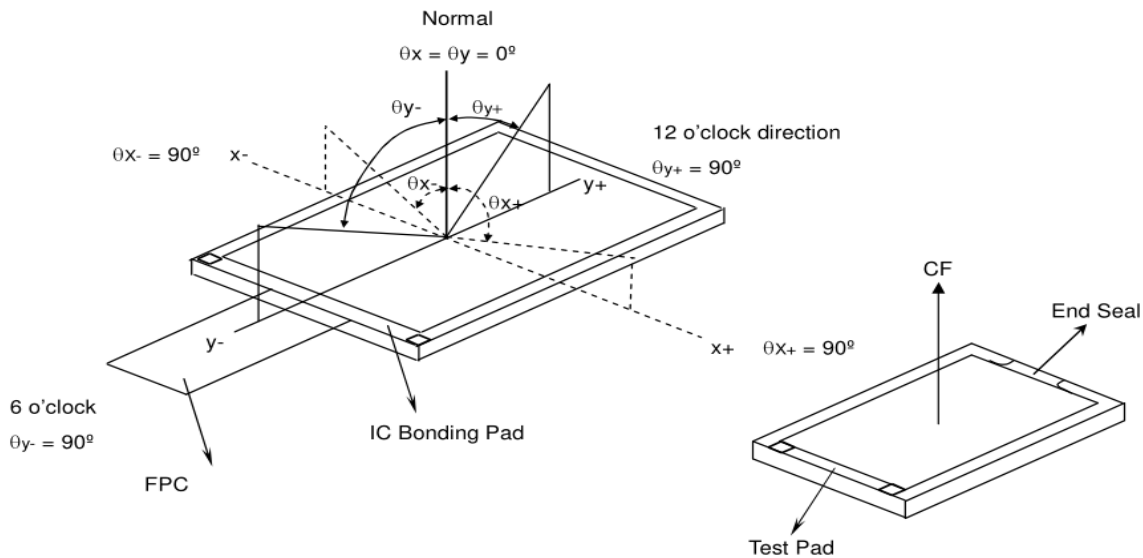
均匀度=白色画面下表面亮度最小值 (P1, P2, ……)/白色画面下表面亮度最大值 (P1, P2, ……)



Note4.Viewing angle is the angel at which contrast ratio is greater than a specific value.For TET module,the specific value of contrast ratio is 10.For monochrome and color stn module,the specific value of contrast ratio is2.The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.For more information see FIG3

备注 4. 视角指对比度大于等于一个特定值时的可视范围，对 TFT 屏，对比对特定值为 10，对黑白屏 CSTN 屏，对比度特定值为 2，视角由横轴（X 轴），竖轴（Y 轴）同 Z 轴（垂直与 LCD 表面）之间的夹角来定义，详见 FIG3.。

FIG3 The definition of viewing angle 视角定义



Note5. Surface luminance is the LCD surface luminance with all white pixels,For more information see FIG2.

LV=Average Surface Luminance with all white pixels(P1,P2,.....)

备注 5. 表面亮度是在显示白色画面时，测试的亮度值，详见 FIG2.

Lv=平均的表面亮度（P1，P2，……）

Note6.CIE(X,Y)chromaticity is the Center point value.For more information see FIG2.

备注 6. 选择中心点，分别测试 X，Y 值，详见 FIG2.。

Note7.For Viewing angle and response time testing,the testing date is base on Autronic-Melchers' s ConScope.Series instruments.For contrast ratio,Surface Luminance,Luminance uniformity and CIE,the testing date is base on CS-2000(BM-7/CA310) photo detector.

备注 7. 视角和响应时间，测试数据基于 Autronil Melchers' s Conoscope 系列测试仪器，而对比度，表面亮度，均匀度 CIE 坐标，测试数据基于 CS-2000(BM-7/CA310)光学测试仪。

Note8.For TN type TFT transmissive module,Gray scale reverse occurs in the direction of panel viewing angle

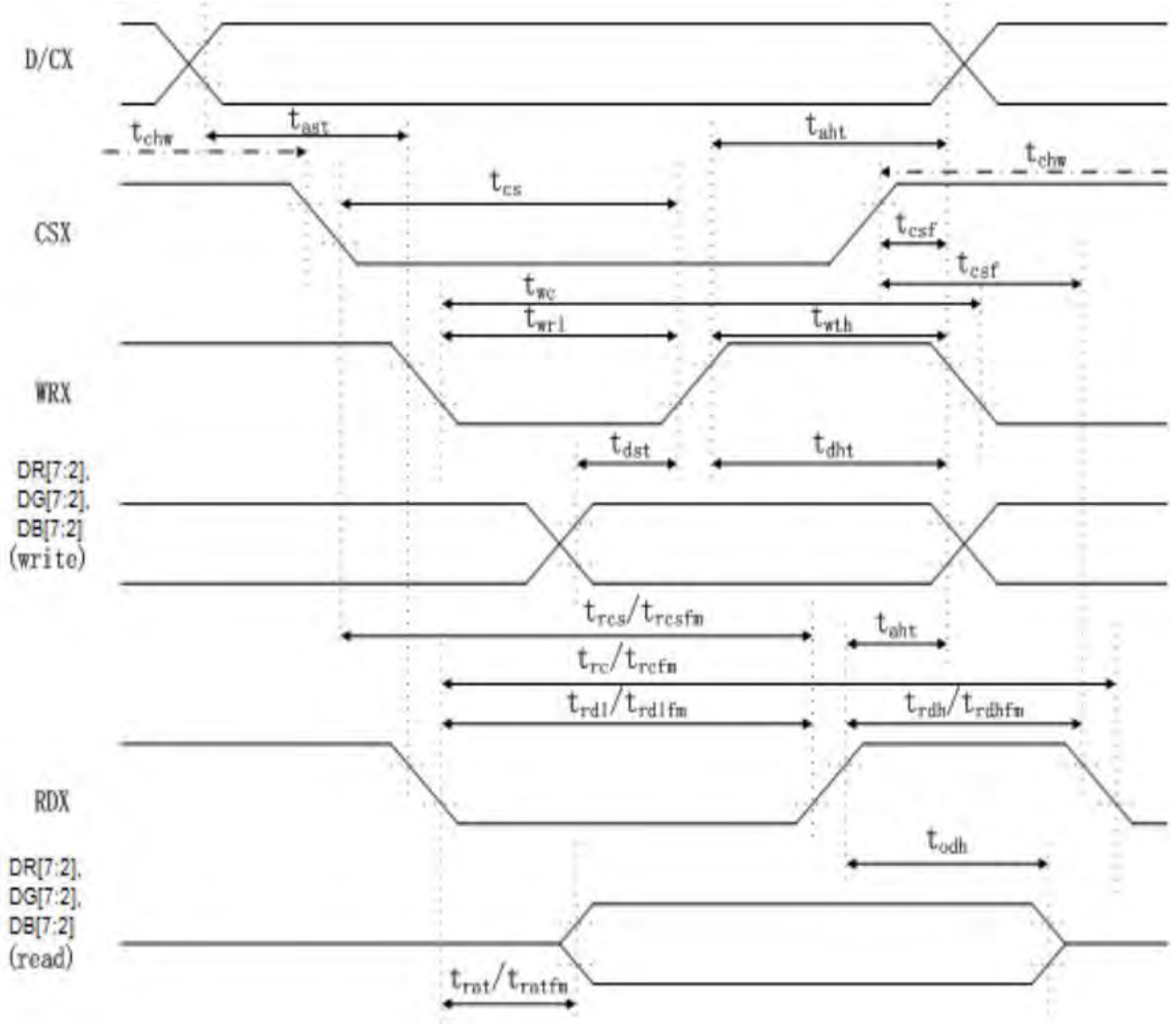
备注 8. TN 型 TFT 全透产品，在视角方向会发生灰度反转。



8. Recommended Operating Sequence

Parallel 8/9/16 bit Interface Characteristics.

(Note: DB[7:2] =D5~D0; DG[7:2] =D11~D6;
DR[5:2] =D15~D12)



Note: Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.



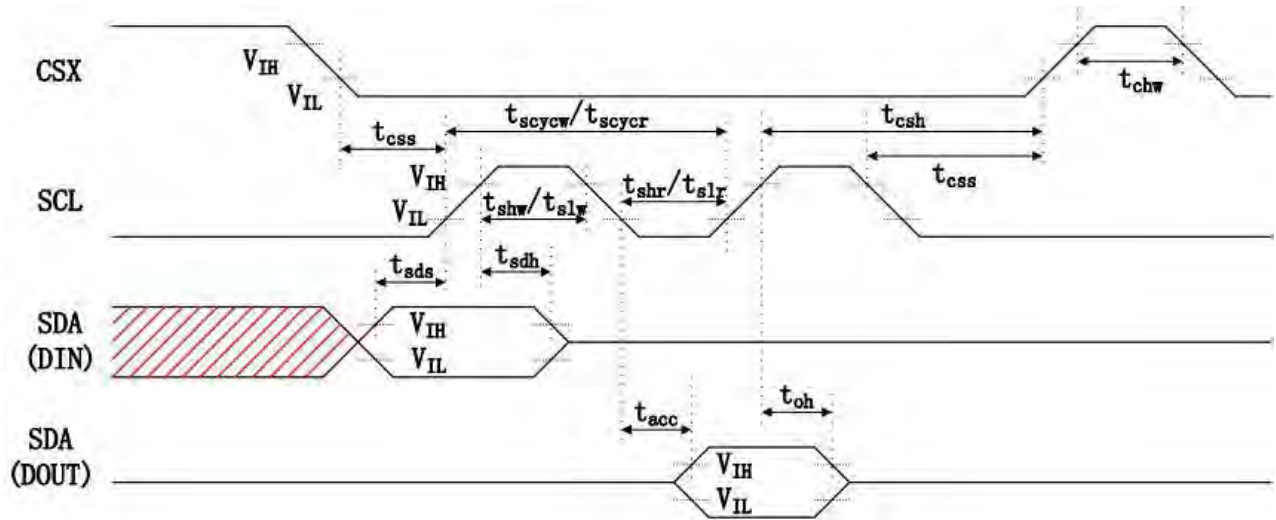
Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
D/CX	T _{AST}	Address Setup Time	0		ns	
	T _{AHT}	Address Hold Time (W/R)	10		ns	
CSX	T _{CHW}	“S” “H” Pulse Width	25		ns	
	T _{CS}	Chip Select Setup Time(W)	10		ns	
	T _{RCS}	Chip Select Setup Time (Read ID)	45		ns	
	T _{RCSFM}	Chip Select Setup Time (Read FM)	355		ns	
	T _{CSE}	Chip Select Wait Time (W/R)	10		ns	
WRX	T _{WC}	Write Cycle	50		ns	MCU 16 Bit Format (5-6-5): T _{WC} >100ns (see “6.4.8.”) MCU 16 Bit Format (6-6-6): T _{WC} >66ns (see “6.4.9.” Figure 6.4.9.4) Other Format T _{WC} >50ns
	T _{WRH}	Control Pulse H Duration	T _{WC} /2		ns	
	T _{WRL}	Control Pulse L Duration	T _{WC} /2		ns	
RDX	T _{RC}	Read Cycle(ID)	160		ns	When Read ID
	T _{RDH}	Control Pulse H Duration(ID)	T _{RC} /2		ns	
	T _{RDL}	Control Pulse L Duration(ID)	T _{RC} /2		ns	
RDX	T _{RCFM}	Read Cycle(FM)	450		ns	When Read From Frame Memory
	T _{RDHFM}	Control Pulse H Duration(FM)	T _{RCFM} /2		ns	
	T _{RDLFM}	Control Pulse L Duration(FM)	T _{RCFM} /2		ns	
DR[7:2], DG[7:2], DB[7:2]	T _{DST}	Data Setup Time	10		ns	CL _{max} =30pF Cl _{min} =8pF
	T _{DHT}	Data Hold Time	10		ns	
	T _{RAT}	Read Access Time(ID)		40	ns	
	T _{RATFM}	Read Access Time(FM)		340	ns	
	T _{ODH}	Output Disable Time	20	80	ns	

Note 1: IOVCC 1.65 to 3.3V, VCI=2.6 to 3.3V, AGND=DGND=0V, Ta=-30 to 70 °C (to +85°C no damage)

Note 2: This input signal rise time and fall time (Tr, Tf) is specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of IOVCC for input signals



3/4-SPI/Dual-SPI/Quad-SPI Interface Characteristics.



Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
CSX	T _{CSS}	Chip Select Setup Time	10		ns	
	T _{CSH}	Chip Select Hold Time	30		ns	
	T _{CHW}	Chip Select "H" Pulse Width	30		ns	
SCL	T _{SCYCW}	Serial Clock Cycle(Write)	12.5		ns	QSPI 4 lane format (5-6-5): T _{scycw} > 25ns (see "6.4.12.") QSPI 4 lane format (6-6-6): T _{scycw} > 16ns(see "6.4.13") Other Format T _{scycw} > 12.5ns
	T _{SHW}	S"L" "H" Pulse Width(Write)	T _{SCYCW} / 2		ns	
	T _{SLW}	S"L" "L" Pulse Width(Write)	T _{SCYCW} / 2		ns	
	T _{SCYCR}	Serial Clock Cycle(Read)	150		ns	
	T _{SHR}	S"L" "H" Pulse Width(Read)	T _{SCYCR} / 2		ns	
	T _{SLR}	S"L" "L" Pulse Width(Read)	T _{SCYCR} / 2		ns	
SDA(DIN) / (DOUT)	T _{SDS}	Data Setup Time	5		ns	CLmax=30pF CLmin=8pF
	T _{SDH}	Data Hold Time	5		ns	
	T _{ACC}	Access Time	5	50	ns	
	T _{OH}	Output Disable Time	10		ns	

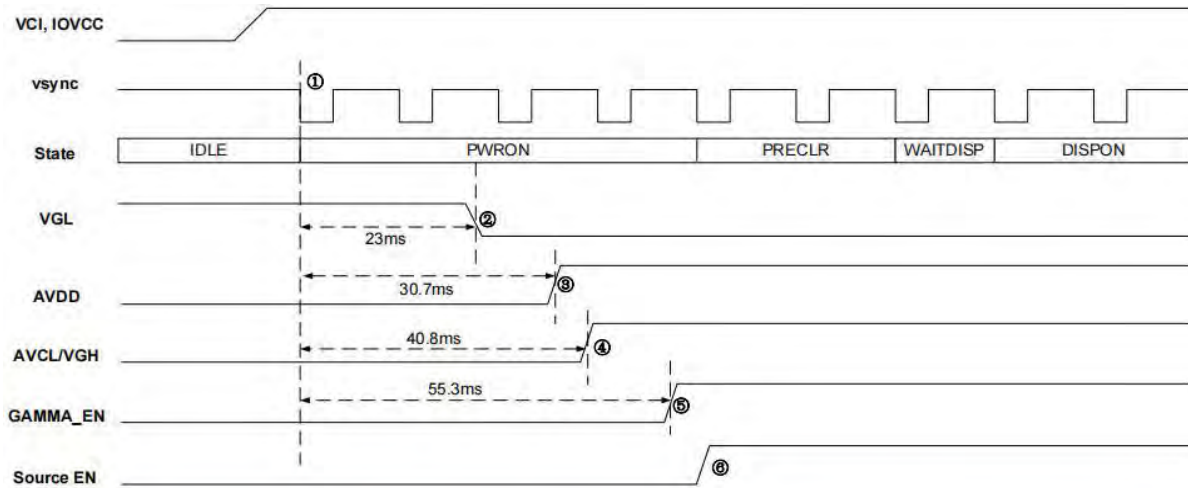


4-SPI Interface Characteristics:

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
CSX	T _{CSS}	Chip Select Setup Time	10		ns	
	T _{CSSH}	Chip Select Hold Time	30		ns	
	T _{CHW}	Chip Select "H" Pulse Width	30		ns	
SCL	T _{SCYCW}	Serial Clock Cycle(Write)	12.5		ns	
	T _{SHW}	S "L" "H" Pulse Width(Write)	T _{SCYCW} / 2		ns	
	T _{SLW}	S "L" "L" Pulse Width(Write)	T _{SCYCW} / 2		ns	
	T _{SCYCR}	Serial Clock Cycle(Read)	150		ns	
	T _{SHR}	S "L" "H" "Pulse Width(Read)	T _{SCYCR} / 2		ns	
	T _{SLR}	S "L" "L" Pulse Width(Read)	T _{SCYCR} / 2		ns	
D/CX	T _{DCS}	D/CX Setup Time	5		ns	
	T _{DCH}	D/CX Hold Time	5		ns	
SDA(DIN) (DOUT)	T _{SDS}	Data Setup Time	5		ns	
	T _{SDH}	Data Hold Time	5		ns	
	T _{ACC}	Access Time	5	50	ns	CLmax=30pF CLmin=8pF
	T _{OH}	Output Disable Time	10		ns	

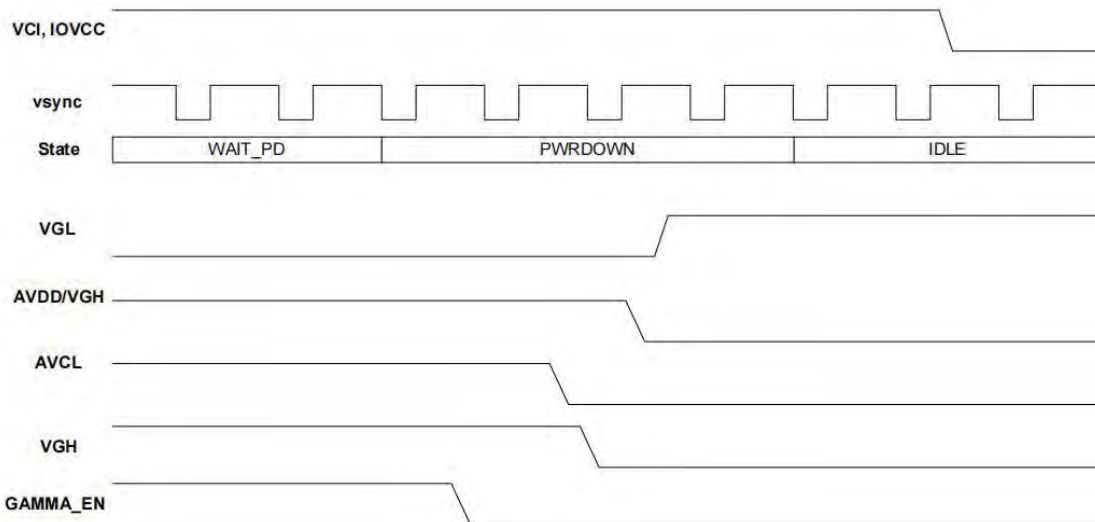


Power ON Timing (VDD=VCI,VDDIO=IOVCC)



No.	Description	Min. Time
1	After VCI/IOVCC become stable , reset finished and host send command "11H"	Decide by host 11H command
2	VGL from 0V to -11.5V	23ms
3	AVDD from VCI to 6.6V	30.7ms
4	AVCL from 0 to -5V and VGH from AVDD to 15.4	40.8ms
5	Gamma output enable	55.3ms
6	Source output enable	Decide by host "29H" command

Power OFF Timing (VDD=VCI)



NOTE: This section is only for reference,Details please refer to the IC specification.

备注：本节仅供参考,详细信息请参阅 IC 规格书。

9. RESET INPUT TIMING

复位时序

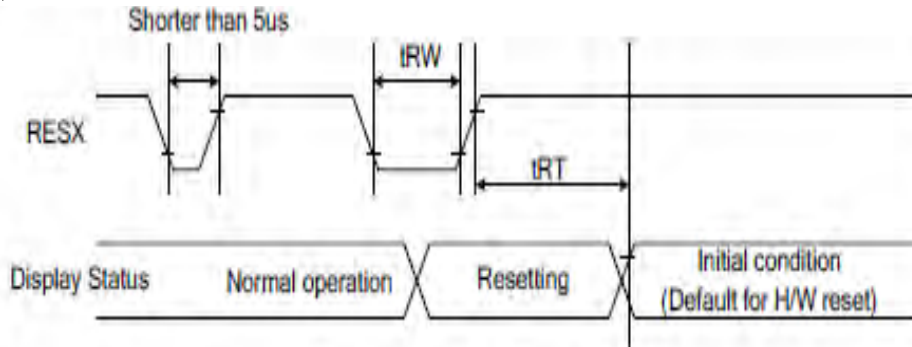


Figure 126: Reset Timing

Table 47: Reset Timing

Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		µS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

Notes:

- The reset cancel also includes required time for loading ID bytes, VCOM setting and other settings from EEPROM to registers. This loading is done every time when there is H/W reset cancel time (tRT) within 5 ms after a rising edge of RESX.
- Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the Table 48.

Table 48: Reset Descript

RESX Pulse	Action
Shorter than 5µs	Reset Rejected
Longer than 10µs	Reset
Between 5µs and 10µs	Reset starts

- During the Resetting period, the display will be blanked (The display enters the blanking sequence, which maximum time is 120 ms, when Reset Starts in the Sleep Out mode. The display remains the blank state in the Sleep In mode.) and then return to Default condition for Hardware Reset.
- Spike Rejection can also be applied during a valid reset pulse, as shown below:

NOTE: This section is only for reference,Details please refer to the IC specification.



备注：本节仅供参考, 详细信息请参阅 IC 规格书

10. Production process control 生产过程控制管理

WORKFLOW CHART

工作流程图

Flow Chart 流程图	Responsibility accounting 权责单位	Related Files 相关文件	Form used 指令表单	Describe 描述
<pre> graph TD A[Production design 生产计划排程] --> B[Production arrangements 生产准备] B --> C[First article production 首件生产] C --> D[First article assurance 首件确认] C -- NC --> D D --> E[Production 生产] E --> F[Finished goods 成品] E --> G[Process Inspection 制程检验] F --> H[Packaging 包装] F -- NG --> I[Rework 返工] H --> J[Final inspection 最终检验] J --> K[Put in storage 入库] J -- NG --> I </pre>	<p>Product Material Control 生产计划与物料控制 Production department 生产部 Production department 生产部 Quality department 品质部 Production department or Quality department 生产部/品质部 Quality department 品质部 Production department 生产部 Production department or Quality department 生产部/品质部 Production department 生产部 Production department or Quality department 生产部/品质部 Production department or storage 生产部/仓库</p>	<p>《Production planning and control documents》 《生产计划控制程序》 《Standard operation procedure》 《作业指导书》 《standard inspection procedure》 《制程检验标准书》 《Corrective and Preventive control procedure》 《纠正预防措施管理程序》 《Finished goods inspection procedure》 《成品检验管理程序》 《Nonconforming Product Control Procedure》 《不合格品控制程序》 《Reprocessing management》 《返工管理办法》 《Material handling management》 《物料搬运管制办法》</p>	<p>Production order 生产指令单 Requisition form 领料单 First article inspection report 首件检验报告 Checklist for process inspection 制程查核表 Notices for corrective and preventive 纠正/预防措施通知表 Certificate of quality 合格证 Storage lists 入库单</p>	<p>Keep good record from raw material input to finished product output . When an exception occurs, find out the reasons , take action to improve it and follow up the effect. we carried out strict control on each working procedure include IPQC check and finished goods inspection . We handle nonconforming product according to nonconforming Product Control Procedure. 从材料输入到输出产品各过程中, 按规定做好相关记录, 发生异常时, 查找原因, 制定改善措施, 并追溯实施效果。 IPQC 巡检或成品检验过程中标识合格品方可转入下一工序。不合格品标识隔离依《不合格品控制程序》处理</p>

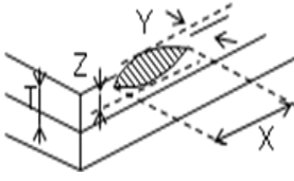
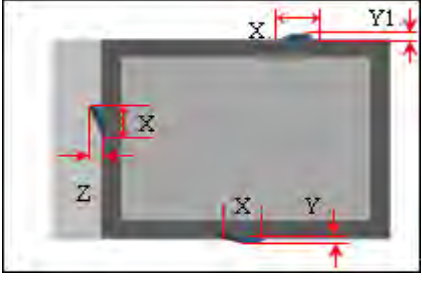
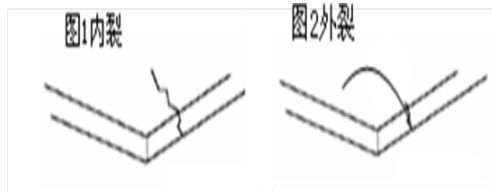
11. INSPECTION SPECIFICATIONS

品质检验标准



不良项目	判定标准				缺点类别	
点状缺陷		尺寸		允许个数	轻缺	
		$D \leq 0.10$		忽略不计		
		$0.10 < D \leq 0.15$		2 密集不可		
		$0.15 < D \leq 0.2$		1		
		$D > 0.2$		0		
亮点	1. 单一点: 单个 Pixel	单一点	2		轻缺	
暗点	2. 连续点: 相连 2 个 Pixel	2 连续亮点	1			
	3. 亮点及暗点总个数不可超过 5 个	单一点	5			
		2 连续暗点	1			
Mura	斑点、不均	参照 LCD 原厂判定标准, 用 ND:5%			轻缺	
电测线状缺陷		尺寸		允许个数	轻缺	
		$W \leq 0.03$		忽略不计		
		$L \leq 5.0, 0.03 < W \leq 0.05$		1		
$W > 0.05$		依点状缺陷判定				
外观线状缺陷	L: 长度 W: 宽度 线不良间距需 $\geq 10.0\text{mm}$	尺寸		允许个数	轻缺	
		$W \leq 0.03$		忽略不计		
		$L \leq 10.0, 0.03 < W \leq 0.05$		2 个		
$W > 0.05$		依点状缺陷判定				
LCD 角破损 (端子位)		长度 X	宽度 Y	高度 Z	允许数	轻缺
		$X \leq 2.0$	$Y \leq 2.0$	$Z \leq T$	2	
		$2.0 < X \leq 3.0$	$2.0 < Y \leq 3.0$	$Z \leq T$	1	
		$X > 3$	$Y > 3$	$Z \leq T$	0	
LCD 边破损 (端子位)		1. 长度 $X \leq 5.0$ 宽度 $Y \leq 1.0$ 2. 崩裂位置不可伤及 ITO 线路、不可影响 bonding				轻缺
LCD 角破损 (非端子部)		长度 X	宽度 Y	高度 Z	允许数	轻缺
		$X \leq 2.0$	$Y \leq 2.0$	不计	2	
		$2.0 < X \leq 3.0$	$2.0 < Y \leq 3.0$	不计	1	



	崩裂位置不可伤及线路	$X > 3$	$Y > 3$		0	
LCD 边破损 (非端子部)	 <p>崩裂位置不可伤及线路</p>	长度 X	宽度 Y	高度 Z	允许数	轻缺
		$X \leq 7.0$	$Y \leq 0.5$	$Z \leq T/2$	2	
		$X \leq 7.0$	$0.5 < Y \leq 0.8$	$Z \leq T$	1	
		$X > 7.0$	$Y > 0.8$		0	
玻璃突出		1. Y、Y1、Z 均 ≤ 0.2 且 $Y+Y1 \leq 0.2$ 时 X 长度不计 2. Y、Y1、Z > 0.2 或 $Y+Y1 > 0.2$ 时， 不允许				轻缺
LCD 裂纹		1: 内裂发生不可。 2: 外裂修正以玻璃破损规格判定, 但不能有可清除的玻璃屑附着。				重缺
外观检查	CF 偏光片保护膜上划线不可有、气泡不可有、盖章不可有 (依客户特殊要求)					轻缺

Note;The rest of the test items according to our standards

注：其余测试项按我司标准执行

12. RELIABILITY TEST CONDITIONS

可靠性测试条件

NO. 序号	Test Item 实验项目	Test Condition 实验条件	Inspection after test 实验后检查
1	High Temperature Storage 高温存储测试	$80^{\circ}\text{C} \pm 2^{\circ}\text{C} / 48 \text{ hours}$	Inspection after 2~4 hours storage at room temperature ,the sample shall be free from defects:实验结束后, 已测试的 LCD 样品必须在室内正常温湿
2	Low Temperature Storage 低温存储测试	$-30^{\circ}\text{C} \pm 2^{\circ}\text{C} / 48 \text{ hours}$	



3	High Temperature Operating 高温操作测试	70°C±2°C/48 hours	度环境下放置，2~4 小时以上才能进行功能和外观检查，样品不允许有以下缺陷： 1.Air bubble in the LCD;模块中有气泡 2.Sealleak;封口松脱 3.Non-display;不显示 4.missing segments;漏笔 5.Glass crack;玻璃破碎 6.Current IDD is twice higher than initial value. 电流 IDD 大于初始值的两倍。
4	Low Temperature Operating 低温操作测试	-20°C±2°C/48 hours	
5	Temperature Cycle 冷热循环存储	-20°C±2°C~25°C~70°C±2°C*10cycles (30min.) (5min.) (30min.)	
6	Damp Proof Test 防潮测试	55°C±5°C*90%RH/48 hours	
7	ESD Test 抗静电性测试	测试前对样品进行外观和功能测试，采用空气放电，对 TP 进行单次放电，9 个点每一点放电一次，每次间隔≥5s, 9 个点为一个循环。放点电压：±8KV，每一个循环放电后进行检查，如果合格就进行下一循环测试，如果不合格，试验停止，并进行问题判定	

13. PRECAUTIONS FOR USING LCD MODULES

模组使用注意事项

1. Handling precautions 使用注意事项

1.1 The display panel is made of glass and polarizer.As glass is fragile.It tends to become or chipped during handing especially on the edges.Please avoid dropping or jarring.Do not subject it to a mechanical shock by dropping it or impact.

显示屏由玻璃和偏光片组成，由于玻璃是脆的，使用过程中要特别注意边缘区，请防止跌落或震动，不能机械碰撞。

1.2 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.Do not touch the display with bare hands,This will stain the display area and degraded insulation between terminals(some cosmetics are determined to the polarizer)

请勿施加过大的压力与显示屏或连接部位，否则会引起色调变化，不要用手接触显示屏，这将弄脏显示区和降低端子之间的绝缘能力，（一些外观是由偏光片决定的）

1.3 The polarizer covering the display surface of the LCD module is soft and easily scratched.



Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in container before coming in to contact with room temperature air.

覆盖液晶显示模块显示平面的偏光片是软性且易被擦伤，请小心轻拿，请勿用任何硬度大于 HB 铅笔芯的物品（玻璃，镊子等）接触、撞压或摩擦裸露偏光片，不要放置或粘附物体在显示区域上以免留下痕迹，冷凝在表面和端子将会损坏或弄脏偏光片，产品在低温下测试之后，与室温空气接触之前必须在容器内升温。

1.4 Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

使用工具如电烙铁，要可靠接地，并确保烙铁使用交流电，不要漏电，用电批固定模块时，电批应接地，尽可能降低电动换向器火花产生的电磁波。

2. Handling precaution for LCM 模块操作规范

2.1 LCM is easy to be damaged. Please note below and be careful for handling.

液晶显示模块很容易被损坏，请注意以下并小心操作

2.2 Correct handling; 正确操作



As above picture, please handle with anti-static gloves around LCM edges.

像上面的图片，请戴防静电手套，并拿模块边缘。

2.3 Incorrect handling 错误操作



Please don't stack LCM
不要把模块叠在一起



Please don't operate with sharp stick such as pens
请不要用尖锐的物体来操作，例如用笔尖

2.4 Input logic voltage before apply analog high voltage such as LCD driving voltage when



power on.Remove analog high voltage before logic when power off the module.Input each signal after the positive/negative voltage becomes stable.

开机时，先开逻辑电压，再接通模拟电压，如显示屏驱动电压。关机时，先断开模拟电压，再关逻辑电压，正负电源都稳定后再送控制信号。

2.5 If the LCD modules have been operating for a long time showing the same display patters,the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear.A normal operating status can be regained by suspending use for some time.It should be noted that this phenomenon does not adversely affect performance reliability.

如果液晶显示模块长时间工作于同一个显示图案，换屏时会出现鬼影，也会出现轻微的对比度不均，停止使用一段时间后可恢复到正常状态，此现象不会严重影响性能可靠性。

2.6 Please keep the temperature within the specified range for use and storage.Polarization degradation,bubble generation or polarizer peel-off may occur with high temperature and high humidity.

模块在操作和存储规范范围内使用，高温高湿可能会引起偏振退化，气泡，偏光片脱落等问题。

3. Storage Precautions 存储注意事项

3.1 When storing the LCD modules,the following precaution are necessary.

液晶显示模块的存储依照以下几点：

3.2 Store them in sealed polyethylene bag.If properly sealed,there is no need for the desiccant
使用聚乙烯密袋封，如果密封得当，不需要干燥剂。

3.3 Store them in a dark place.Do not expose to sunlight or fluorescent light,keep the temperature between0~35°C,and keep the relative humidity between40%RH and 40%RH.

避光保存，避免直接暴晒在太阳光或黄光灯下，保持温度在 0~35°C 之间，保持相对湿度在 40% RH 和 60%RH 之间。

3.4 The polarizer surface should not come in contact with any other objects(We advise you store them in the anti-static electricity container in which they were shipped)

偏光片表面避免接触其他物质（建议存放在货运防静电包装中

4 Transportation Precautions 运输注意事项

4.1 During shipment,pleas handle with care.The packaging bag can not be broken,step on trap.Packing Carton layer height can not be over two meters .

装运过程要轻拿轻放，不能出现包装袋破损，塌陷，卡通箱叠层高度不能超过两米。

4.2 The transportation process should pay attention to the waterproof and moisture-proof measures.Product can not be watering.Ethylene sealed bags can not be unsealed.

运输过程要注意有防水和防潮措施，产品不能淋水，产品乙烯密封袋不可拆封。

14. PACKING SPCIFICATION

包装规格(370*300*150)



The top of the empty pallet
最上边空托盘：1PCS

Pear pad each finished 1PCS
珍珠垫每盘成品上1PCS

Two pallets stacked cross 180 degrees
Stacked layer has 11 layer, including
the top plate

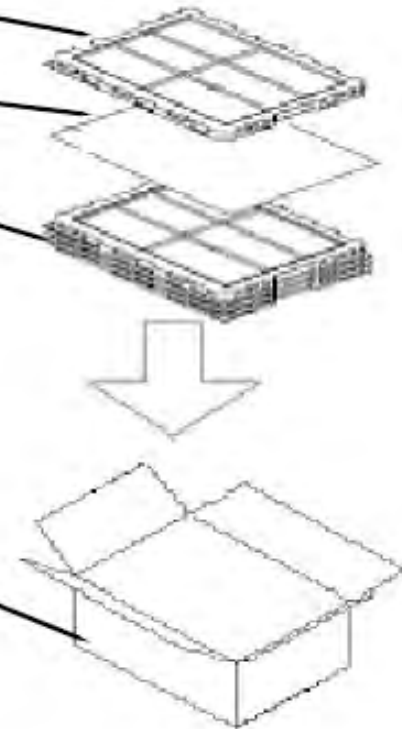
两个托盘需要交叉180度叠放
叠放层数为11层，包括顶层面空盘

Outer box: 1PCS

Specifications: 390*290*150MM

外箱：1PCS

规格：390*290*150MM



15. SPECIAL REMAKS

注意事项

1. The above specifications are the binding criteria for Yousee Technology's outgoing quality inspection.
以上规格描述为宇锡的品质出货标准
2. The customer is kindly requested to inform Yousee Technology as soon as possible on any questions, remarks, and disagreements regarding these specifications.
对于规格中的任何问题或存在疑问，客户可随时向宇锡公司进行咨询。
3. Yousee is not responsible for damage to its products due to neglect of the precautions as described in the previous chapter.
如果不按照规格书要求进行操作而损坏产品的，宇锡不承担责任。
4. About the limited warranty unless special agreement between Yousee and customer Yousee will replace or repair any of its products that are found to be functionally defective when inspected in accordance with Yousee acceptance standards for a period of one year from data of shipments.
除宇锡跟客户签定协议外，对确认为属于产品本身功能性缺陷的，在宇锡可接受范围内可进行退换或维修，宇锡保质期为从出货日期起一年内有效。